

Green Energy Management in Electric Vehicles with Regenerative Braking Using Super capacitors and Batteries

**Kannan Kandavelu^{1*}, Mahalakshmi Gunasekaran², Anbuchejian Ashokan³,
Silambarasan Rajendran^{4,5}**

¹Department of Electrical and Electronics Engineering, Annapoorana Engineering College (Autonomous), Seeragapadi, Salem, 636308, Tamil Nadu, India.

²Department of Electrical and Electronics Engineering, Sri Krishna College of Engineering and Technology (Autonomous), Coimbatore, 641008, Tamil Nadu, India.

³Department of Civil Engineering, Annapoorana Engineering College (Autonomous), Seeragapadi, Salem, 636308, Tamil Nadu, India.

⁴Department of Mechanical Engineering, Annapoorana Engineering College (Autonomous), Seeragapadi, Salem, 636308, Tamil Nadu, India.

⁵Department of Mechanical Engineering, Saveetha School of Engineering, Saveetha Institute of Medical and Technical Sciences, Chennai, Tamil Nadu, India.

***Corresponding author: k.kannan79@gmail.com**

ABSTRACT

This work presents the design and implementation of an energy management system for electric vehicles utilizing regenerative braking. The hybrid power supply comprises a lithium-ion battery bank and a supercapacitor bank. Bidirectional DC-DC converters are employed to manage power flow between the energy storage elements and the DC motor. Regenerative braking allows kinetic energy recovery during deceleration, which is stored in the supercapacitors due to their high power density. The supercapacitors also handle high current demands during acceleration, reducing battery stress. Passive equalization circuits are used for the batteries and supercapacitors to avoid overvoltage conditions. The control system is implemented in MATLAB/Simulink and hardware-in-the-loop testing is performed with a dSPACE platform. Experimental results demonstrated up to 65% regeneration efficiency, with good agreement between simulated and practical values. The supercapacitors successfully supplied momentary high current loads, avoiding premature current limitation by the battery protection system. The results confirm the feasibility of the proposed architecture for electric vehicle applications requiring high power and energy recovery.

Keywords: Regenerative braking, Supercapacitors, Energy management, Electric vehicles, Bidirectional DC-DC converters

Nomenclature

SOC- State of Charge

HESS- Hybrid Energy Storage System

SC- Supercapacitor

BMS- Battery Management System

PI Controller- Proportional-Integral Controller

PWM- Pulse Width Modulation

DC-DC Converter- Bidirectional converter for managing energy flow

Regen Braking- Regenerative braking (braking that converts kinetic to electrical energy)

η_{regen} - Regenerative braking efficiency

1. Introduction

Electric vehicles have gained increasing attention in recent years due to environmental concerns and the need for sustainable transportation solutions. However, some key limitations persist, including relatively short driving range and high battery costs. Research has focused on hybrid energy storage systems and regenerative braking capabilities to address these issues. This work explores an electric vehicle architecture consisting of a lithium-ion battery bank as the main energy source, supplemented by a supercapacitor bank to handle high power demands. Bidirectional DC-DC converters are designed to control power flow between the storage elements and the traction motor.

During deceleration, the motor can operate as a generator to recover some of the vehicle's kinetic energy. This regenerative braking process charges the supercapacitors, using their high-power density and efficient charging capabilities. The supercapacitors also supply momentary high current loads during acceleration events, avoiding over-stressing the

batteries. Passive balancing circuits are implemented for the supercapacitors and batteries to prevent overvoltage shutdowns.

The proposed system is modeled in MATLAB/Simulink, allowing rapid design iteration through simulations. The control logic is deployed on a dSPACE real-time platform to enable hardware-in-the-loop testing. Custom circuit boards were developed for the DC-DC converters, gate drivers, sensors, and auxiliary systems. The experimental test bench uses a permanent magnet DC motor coupled to a flywheel load.

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Various operating scenarios are evaluated, including acceleration, constant speed cruising, and regenerative braking. The supercapacitors successfully handle the high current transients during acceleration and charging, while the batteries provide steady discharge currents. Energy transfer efficiencies up to 65% are demonstrated during braking, reasonably matching theoretical predictions. The results help validate the concept of hybrid energy storage and bidirectional power flow for electric vehicle applications.

Future work may involve integrating a grid-connected battery charger to enable full vehicle system implementation. Advanced control algorithms could also improve efficiency and expand operational range. Electric vehicles can become more viable transportation alternatives by overcoming limitations such as short driving distance per charge. The hybrid energy supply and regenerative braking techniques explored in this project represent important steps toward sustainable and efficient electric mobility.

2. Materials and Methods

2.1 Circuit Design and Simulation

The overall system was modeled in MATLAB/Simulink to allow rapid design iterations through simulations. The bidirectional DC-DC converters were designed to interface the battery, supercapacitor, and DC motor. A buck-boost topology was chosen for its ability to step up and down voltage as needed. The converters were controlled using PI controllers to regulate current flow based on reference values. PWM signals were generated to drive the converter switches. Simulations were run to tune the control loop gains and validate expected performance.

The battery bank consisted of 27 lithium-ion cells connected in series, offering a nominal 96V output. Passive balancing circuits were implemented to avoid cell under or overvoltage conditions during operation. The supercapacitor bank contained 23 modules rated at 58F, 16V, connected in parallel combinations to provide 134V nominal voltage.

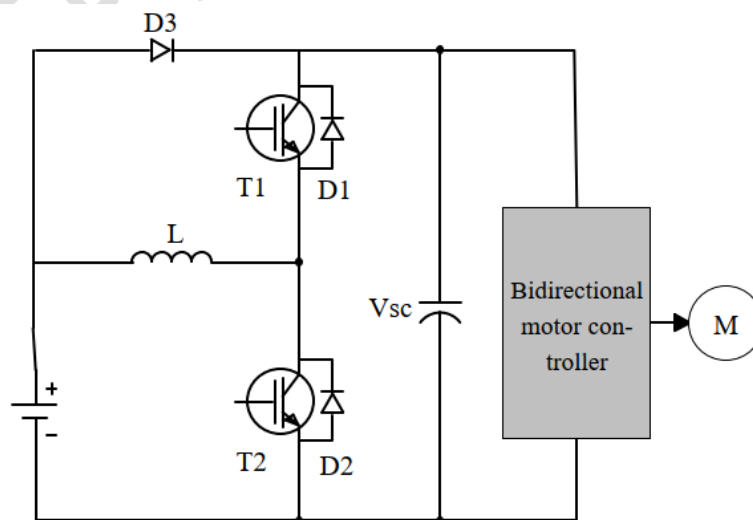


Figure 1 Hybrid Energy Management System.

Differently from the projects presented in Figure 1, it maintains the supercapacitor bank connected directly to the DC bus to supply the power peaks required by the electric motor. The battery bank, positioned on the side of lower tension, is connected to the supercapacitor bank through an energy converter and by the diode D3 (34).

This system operates according to the vehicle's power requests. In conditions normal operating conditions, the battery supplies energy to the DC bus and thus the voltage of the supercapacitors can be maintained at high levels and greater than those of the other source (in this case if diode D3 is reverse biased). Suppose the power request is greater than the nominal capacity of the bidirectional DC-DC converter (acceleration process). In that case, the voltage at the bus cannot be maintained, and so the battery supplies power through diode D3 and also by the converter.

During the regeneration process, energy flows directly to the supercapacitors. Suppose the bus voltage rises above the maximum values permitted. In that case, energy will be sent to the batteries by operation as a step-down of the converter bidirectional. In this way, the battery is charged in a controlled manner. The bidirectional DC-DC converters presented so far are capable of process energy in two directions, but performing only one function (lowering or elevator). Generally, the converter operates as Buck in braking processes regenerative and as a Boost in acceleration processes. However, if the bus voltage is less than that of the battery, the converter operations must be reversed. That way, a universal bidirectional converter capable of operating in both directions must be used and with lifting and lowering functions (38). The circuit that meets these requirements is represented in Figure 2.

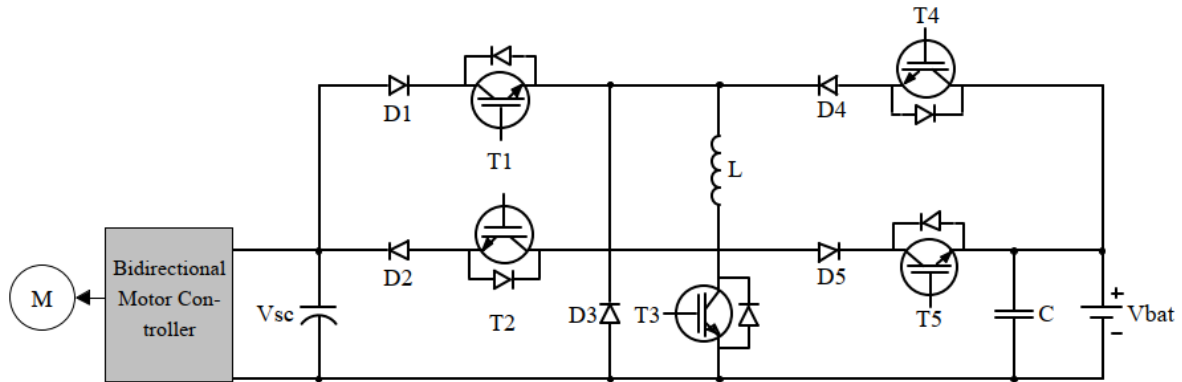


Figure 2 Universal bidirectional DC-DC converter.

An embedded control system was modeled to manage power flow between sources based on SOC and vehicle acceleration/braking conditions. This high-level controller determined the DC-DC converters' reference currents and the converter switches' gate signals. Protection systems were incorporated to disable operation during fault events.

This converter is composed of five transistors (T1 to T 5) with their respective diodes in antiparallel and with two power diodes (DI to DS), during operation only one of the transistors operates through gate pulses (PWM), and the others they remain running or open depending on the purpose performed. Thus, losses due to switching are smaller than those of conventional converters (38). Suppose the supercapacitor voltage is lower than the battery voltage. In that case, the converter must operate as an elevator to bring the energy of regenerative processes to the second source - In this case, transistors T1 and T5 remain in conduction, while T2 and T4 remain turned off; the P WM Signal is applied to T3 (the Boost converter is composed of the elements, T3, T5, L, DI and DS). Maintaining the same condition, the converter must operate as a step-down in the processes acceleration, sending battery energy to the DC bus (where the bank connects of supercapacitors) - The transistors T 1, T 3 and T 5 remain off, while T2drives all the time; the P WM Signal is applied to T4 (the Buck converter is composed by elements T2, T4, L, D2, m and D4) (38).

Hardware Implementation

A scaled-down prototype was built with a 90V battery bank, 66V supercapacitor bank, and 24V permanent magnet DC motor coupled to a flywheel load. The bidirectional converters were constructed using MOSFETs with high current capability and fast switching

speeds. Gate driver circuits were implemented to amplify the PWM signals from the DSP and provide isolation.

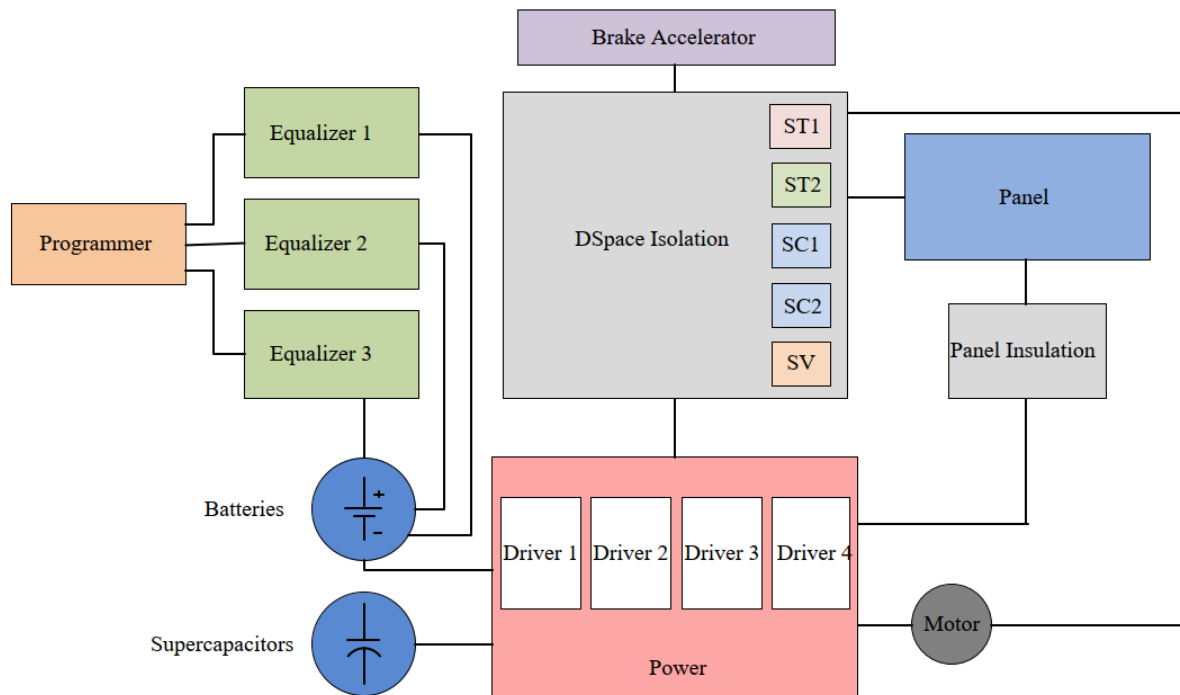


Figure 3 General diagram of the bench prototype.

The "Programmer" board is tasked with programming the dedicated integrated circuits (ICs) for battery equalization, setting critical parameters such as minimum voltage thresholds and operational limits for overload and short-circuit protection during charging and discharging cycles, among other functionalities as shown in Figure 3.. The "Equalizer", "Equalizer 2", and "Equalizer 3" boards host the equalization circuits and auxiliary power sources, and also incorporate additional components for battery temperature equalization, including power transistors and resistors. Each board also features connectors designed for the electrical linkage of battery units.

The "DSpace Isolation" board is equipped with auxiliary power sources, digital and analog isolators, and operational amplifiers. Its primary role is to safeguard the DSpace platform and the connected computer system against potential issues or accidents arising from other systems. Furthermore, the setup includes five small modules: "STL" (Voltage Sensor 1), "ST2" (Voltage Sensor 2), "SCI" (Correction Sensor 1), "SC2" (Current Sensor 2), and "SV" (Speed Sensor), which are integral to the preprocessing of signals and corrections.

The "Accelerator and Brake" system comprises two potentiometers; one regulates the armature voltage of the DC motor during operation, while the other modulates the braking current, which is directly proportional to the braking torque.

The "Panel" features displays for monitoring battery voltages and uses LEDs (Light-emitting diodes) to indicate the system's current status. It is also equipped with interfaces for signaling the presence or absence of power at the charging station (PA), activating or supplying gate pulses to the IGBT (Insulated-Gate Bipolar Transistor) drivers, or performing a system reset. The "Insulation Panel" adapts and isolates the voltages from the battery bank and supercapacitors, preparing them for further processing.

Finally, the "Power" board aggregates key components of the bidirectional converter and includes auxiliary power sources, current sensors, and IGBT drivers (Driver 1, Driver 2, Driver 3, and Driver 4), all interconnected via connectors. Each driver is tasked with receiving gate pulses (0 and 5V) and converting them to 15V with galvanic isolation, utilizing an isolated power source that maintains the same potential at its output.

Hall effect sensors measured the inductor currents for feedback control. The DC motor had an integrated tachometer to measure shaft speed. Analog signal conditioning circuits adapted the sensor outputs for the digital controller. Optocouplers provided isolation for fault signals and control inputs like accelerator/brake pedal position. Custom PCBs were fabricated for the converters, gate drivers, and auxiliary systems.

The control logic and regulators were coded in Simulink then compiled to run on a dSPACE DS1006 processor board. The dSPACE provided ADCs, PWM outputs, digital I/O, and interfacing with MATLAB/Simulink for hardware-in-the-loop testing. Protections were included to disable operation during overcurrent or voltage faults. Data was logged in the dSPACE ControlDesk interface.

3. Experimental Setup and Procedure

The test bench was used to evaluate the performance of the prototype. Acceleration tests verified the supercapacitors could supply the temporary high current loads. Constant speed and cruising scenarios validated steady-state operation. Regenerative braking tests were

conducted by increasing the motor load to demonstrate charging of the supercapacitors from recovered energy. The dSPACE controller successfully coordinated the switching states of the converters in response to operating conditions.

Current, voltage, and speed measurements were recorded to quantify the energy flows during the tests. Regeneration efficiency was determined based on the energy recovered versus the maximum possible. Results were analyzed relative to theoretical predictions from simulations. This experimental evaluation demonstrated the feasibility and effectiveness of the hybrid energy storage system with bidirectional converters. The test bench provided a flexible platform for validating the system integration before installing in an actual vehicle.

The project's circuitry and their respective boards provide a comprehensive view of the energy management and conversion processes, augmented by a control system implemented in Simulink and executed on the platform. The DC motor utilized in this setup is equipped with a tachometer attached to its shaft, facilitating the acquisition of angular velocity data for the associated preprocessing module.

At the opposite end, a disk with a mass of 4.27 kg, a radius of 21.3 cm, and a moment of inertia of $0.097 \text{ kg}\cdot\text{m}^2$ simulates the inertia of a small-scale vehicle. This setup poses a question on efficiently capturing the necessary kinetic energy during regenerative braking phases. The entire assembly is currently stationed in the laboratory. After introducing all system components, their complete interconnection is showcased in Figure 4. Connections between digital outputs and inputs were established using pairs of headers connected by flat cables, chosen for their flexibility and suitability for transmitting low-power signals.

Energy accumulators are linked to the "Power" board via "banana" type connectors, alongside the inductor (L) and the DC motor. A heatsink, approximately covering its perimeter, is situated between the transistors, with forced ventilation provided by a fan located on the left side of the assembly.

The control system's operational signals are transmitted via soldered cables running from the "DSpace+ Isolation" board to the desktop computer, positioned to the right in Figure 4. Finally, the power for all auxiliary sources is supplied by isolated DC-DC converters

powered by this computer's supply, thus eliminating the need for additional hardware within the system.

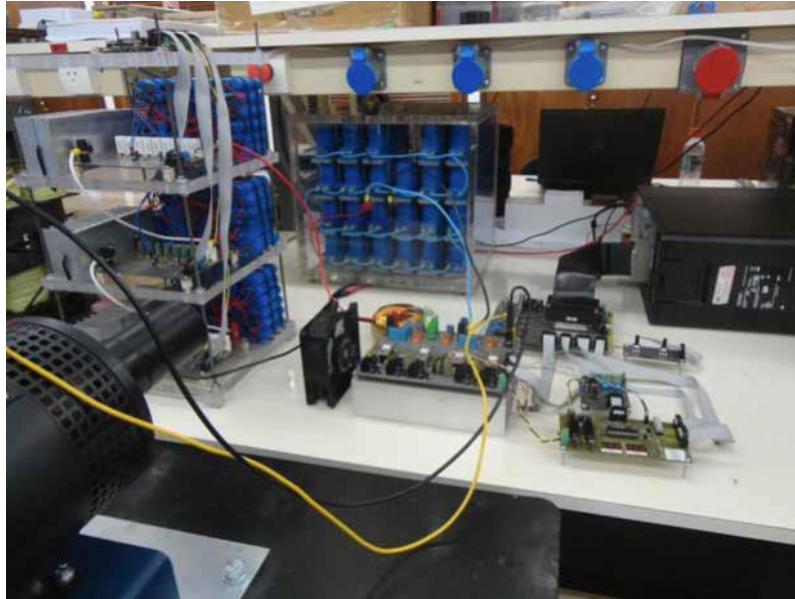


Figure 4 Complete experimental setup visualized.

4. Results and Discussion

The experimental test bench provided the ability to validate the operation of the hybrid energy storage system and control logic. Various scenarios were evaluated to demonstrate the key functions of the prototype, including acceleration events, constant speed cruising, and regenerative braking. During acceleration from standstill, the supercapacitors supplied the temporary high current demanded by the motor. Currents over 8A were drawn, while the batteries provided under 2A, avoiding stress on the cells. The supercapacitors exhibited minimal voltage sag during these heavy loads due to their low equivalent series resistance. As speed stabilized, the batteries became the main power source.

In this section, we delve into the system's overall behavior as observed through simulations conducted within the Simulink environment. The focus here is on outlining the system's operation, which is based on the interactions between batteries, supercapacitors, and a mechanically applied resistive torque, assumed to be constant for the purposes of this discussion. Beginning with the primary inputs—namely, the accelerator and brake signals—we aim to shed light on the dynamic processes outlined in Chapter 3. Furthermore, aspects related to the regeneration of energy and performance metrics during braking phases, along with the corresponding equations, will be scrutinized to assess their accuracy and

predictability.

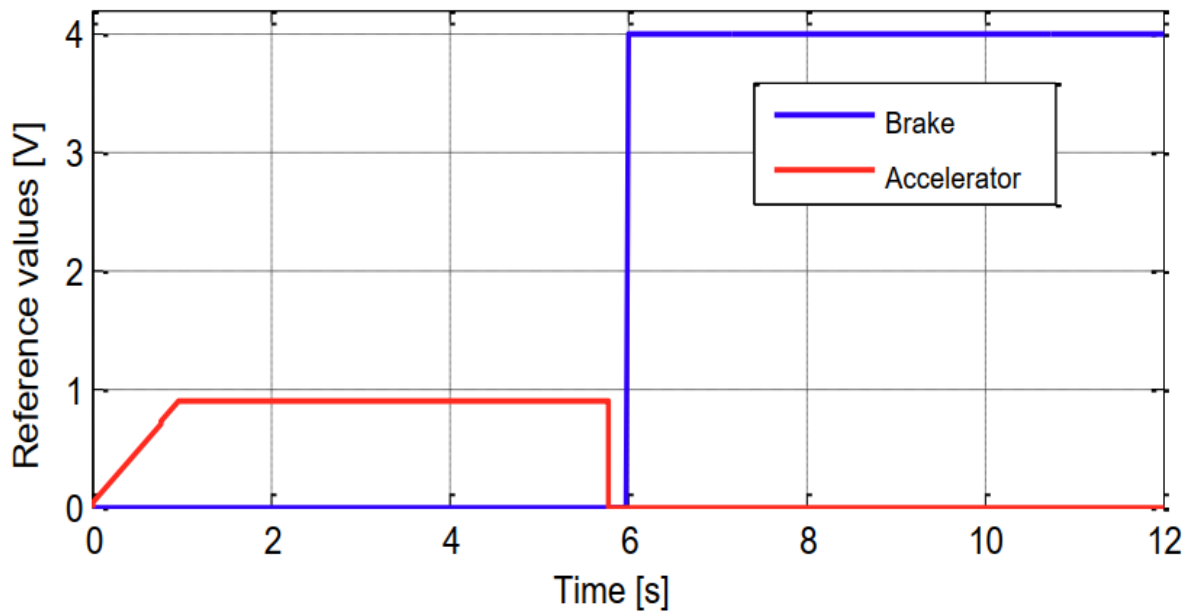


Figure 5 Accelerator and brake signals.

The energy exchange dynamics between the battery accumulators and the current motor are illustrated through colored areas generated in the simulation results, highlighting instances of energy transfer. Within these areas, smaller rectangles identify key elements of the system, where notations such as BSC→M, M→BSC, and BB→BSC represent energy flows from the supercapacitor bank to the motor, from the motor to the supercapacitor bank, and from the battery bank to the supercapacitor bank, respectively.

The initial graphs are derived under $V_{bsc} = 240V$, $V_{eb} = 96V$, and $T_m = 0.50N.m$. Interactions with the accelerator and brake are essential to facilitate the intended system functionalities, as these represent the control inputs from the user. Figure 5 showcases the resultant behavior, providing insight into

The throttle signal indicates that the average voltage at the DC motor terminals reaches 90% (144V) in the interval of 1s, which is maintained up to 5.8s; From then on, there is no further transfer of energy to the machine. The brake is applied at 6s in the form of a step (sudden braking), forcing the absorption of a current I_a equal to 4A, maintained until the end of the simulation. The variation of the motor shaft speed as a function of time based on the behavior of the inputs can be seen in the graph in Figure 6.

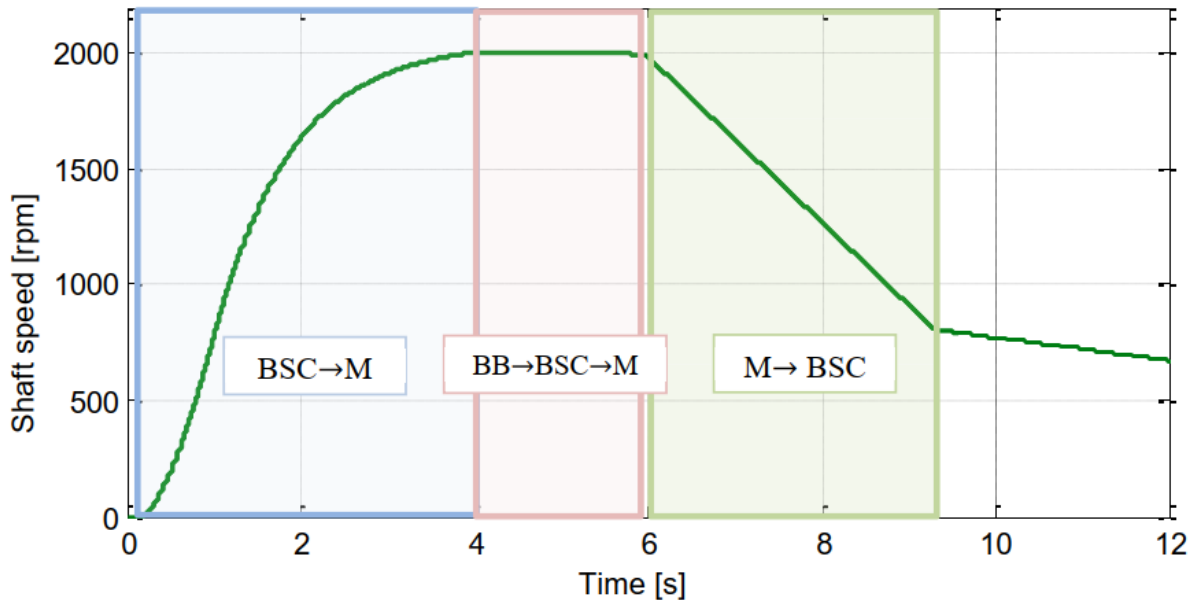


Figure 6 Evolution of ω_n based on accelerator and brake signals.

It is observed that there is acceleration even after the interruption of the voltage increase in the motor terminals, where the speed reaches the steady state in 4s, a period necessary for it to overcome the inertia of the mechanical load connected to the shaft. At the instant of braking, the speed decreases linearly, because the electromagnetic and mechanical resistant torques are constant. The most interesting point occurs at $t = 9.30s$ with the interruption of regeneration even with the persistence of brake actuation, because at this moment $D4$ reaches 0.8. Figure 123 shows the status signals Fr , and $a > 0$, referring to the occurrence of regenerative braking and the angular acceleration of the motor shaft, respectively.

The signals shown in Figure 7 clearly distinguish between the acceleration and braking stages. Thus, it is verified that the first occurs up to approximately 4.04s; that is, only from this point should the battery bank send energy to the supercapacitors in order to maintain its voltage. The second step remains active at the instants of time in which the cyclic ratio $D4$, whose behavior Figure 8 assumes values less than or equal to 0.8.

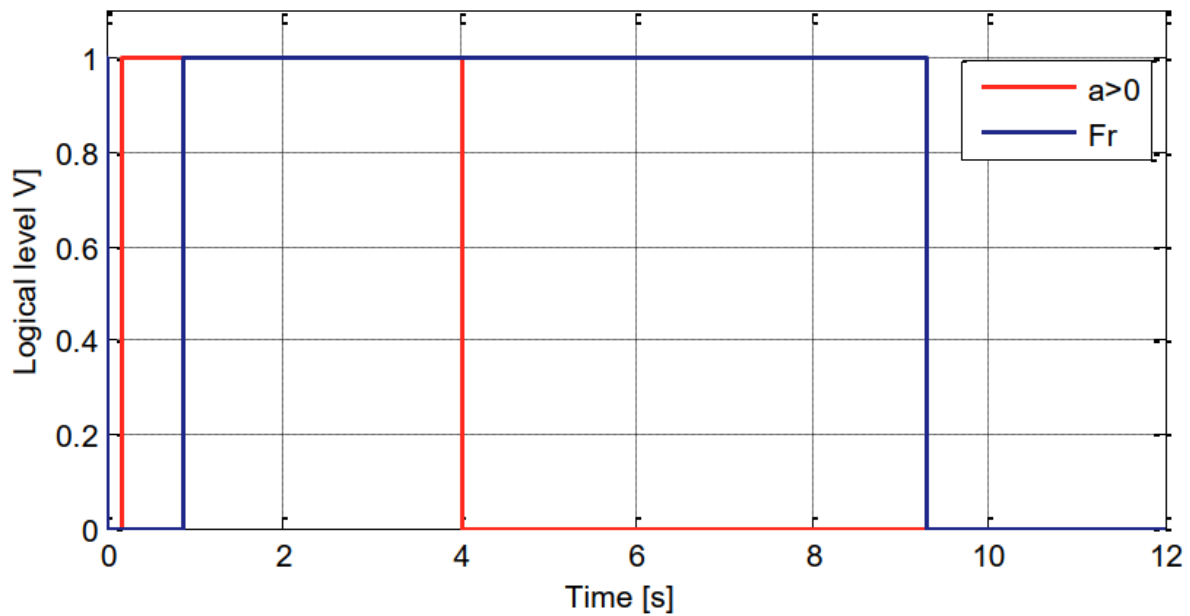


Figure 7 Status signals for acceleration and regenerative braking.

At the beginning of the simulation, D_4 equals the unit, because $cn = 0$, reducing as acceleration occurs. The Fr signal shown in Figure 7 reaches a high logical level whenever $D_4 \leq 0.8$, and this situation indicates that braking is possible and can be initiated at any time. For the conditions adopted, its qualification occurs in 0.88s.

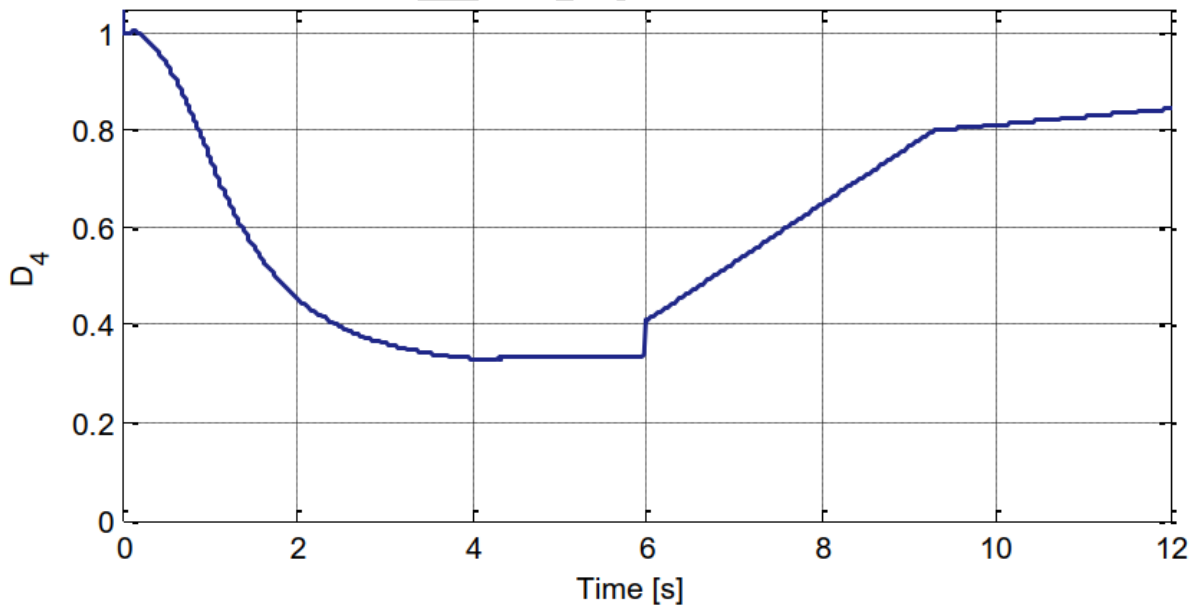


Figure 8 Temporal evolution of $D_4(t)$.

The graph in Figure 9 depicts the behavior of the current in the armature. It is observed that during the variation of the accelerator signal the current increases, because the voltage at the

terminals of the machine is increasing, and from $t = 1$ s it decreases exponentially, becoming constant in the vicinity of $t = 4$ s corresponds to the direction of the reinforcement current, which is positive during traction ($T_e > 0$) and negative during regeneration ($T_e < 0$).

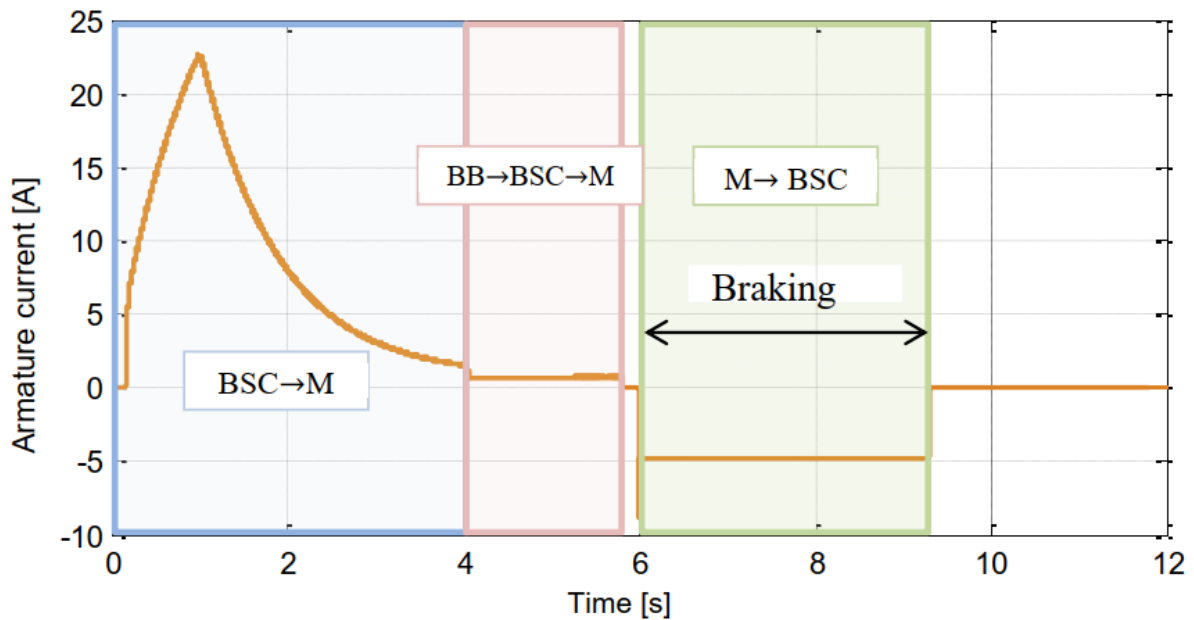


Figure 9 Armature chain in the acceleration and braking stages

When accelerating, all the energy absorbed from the DC motor originates from the supercapacitor bank. Thus, the batteries are used only when the angular velocity ω_n becomes approximately constant, a situation satisfied when the signal $a > 0$ is at a low level and $I_a > 0$. The graph in Figure 10 shows this fact.

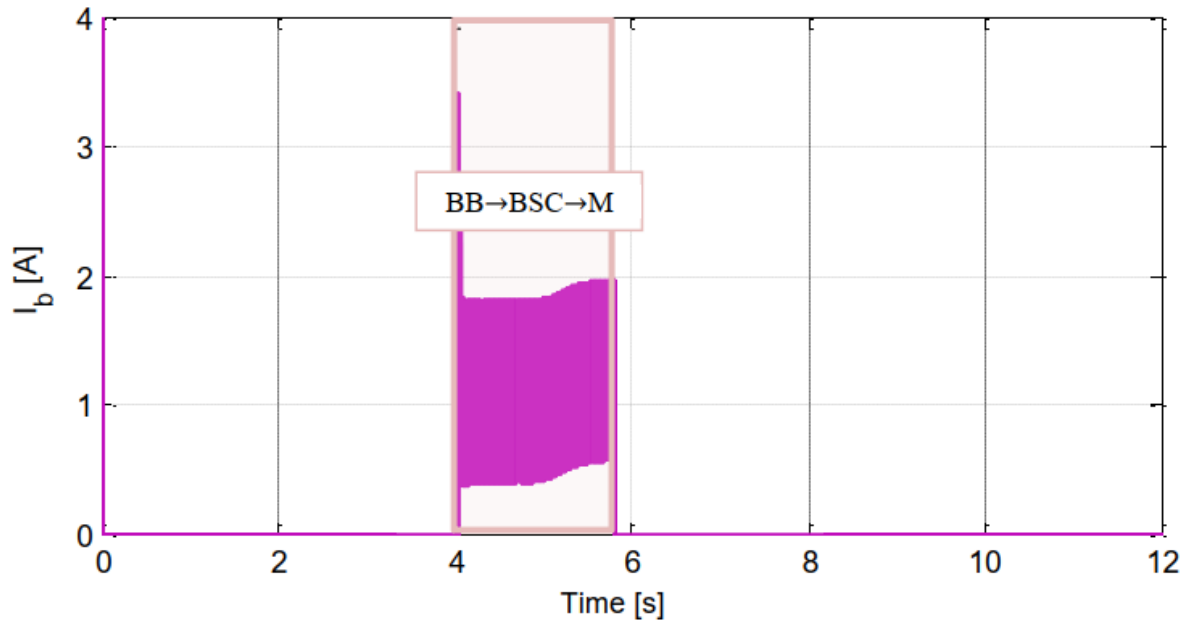


Figure 10 Battery discharge current

As can be seen, the current drained from the battery has a low magnitude since the motor operates with a load of only 0.5N.m, in addition to being zero during acceleration and after the power supply interruption to the armature at $t = 5.8$ s. The energy generated during regenerative braking will be preferentially stored in the supercapacitor bank, thus causing an increase in its voltage, whose behavior is given by Figure 11. A considerable drop in V_{bsc} was observed between $t = 0$ and $t = 1$ s due to the high current absorbed by the armature during this period. The voltages at the beginning and end of braking, equal to 232.68 and 234.72V, respectively, translate to the total amount of energy regenerated, which is equivalent to 1.20kJ.

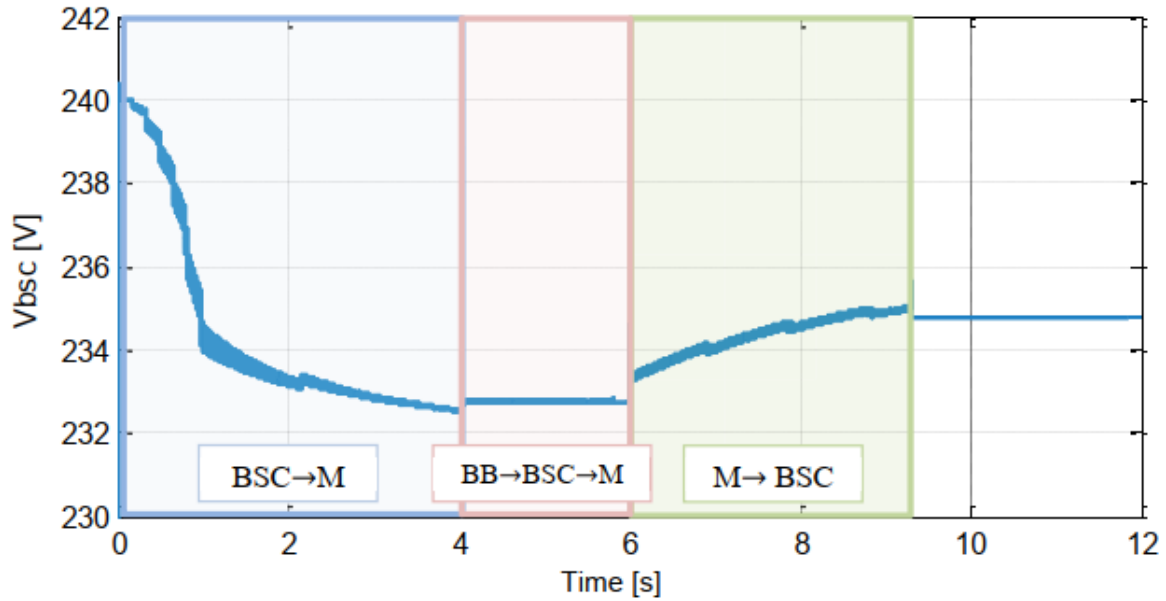


Figure 11 Voltage in the supercapacitor bank as a function of time

In order to illustrate the charging stage of the supercapacitors, the simulation will now be started with $V_{bsc} = 185\text{V}$, keeping the throttle and brake disabled. Right at the beginning, the control system notices that V_{bsc} is less than 190V , then initiates the discharge of the batteries at 1C ($I_b = 15.60\text{A}$), represented in Figure 12. At the end of the process, it was noted that the supercapacitors were not charged to the specified maximum voltage (210V). The reason for this is due to the influence of the R_{sc} series resistor, where the voltage on it is added to the V_{sc} during current injection. However, this is not a problem and can be compensated by increasing the indicative end-of-load value. Figure 13 shows the temporal evolution of V_{bsc} in this period. In order to compare the simulated results with the theoretical ones in terms of performance, Table 18 was elaborated, where the values of mechanical energy, regenerated energy and yield are obtained via Simulink® and respectively by equations (93), (101) and (102).

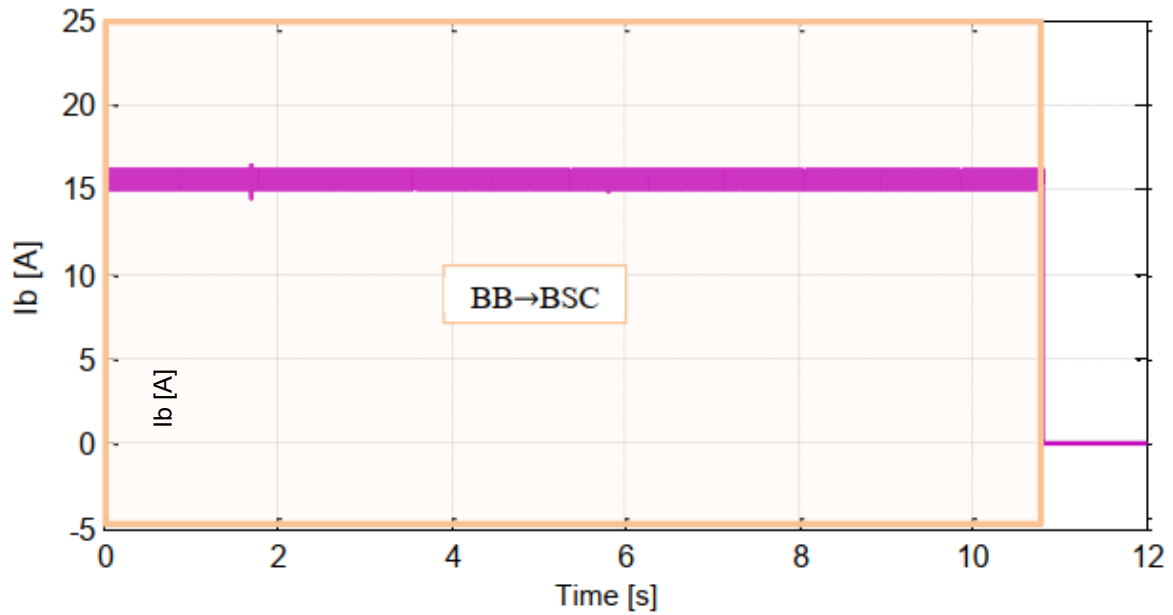


Figure 12 Discharge current of the batteries during the charge of the supercapacitors

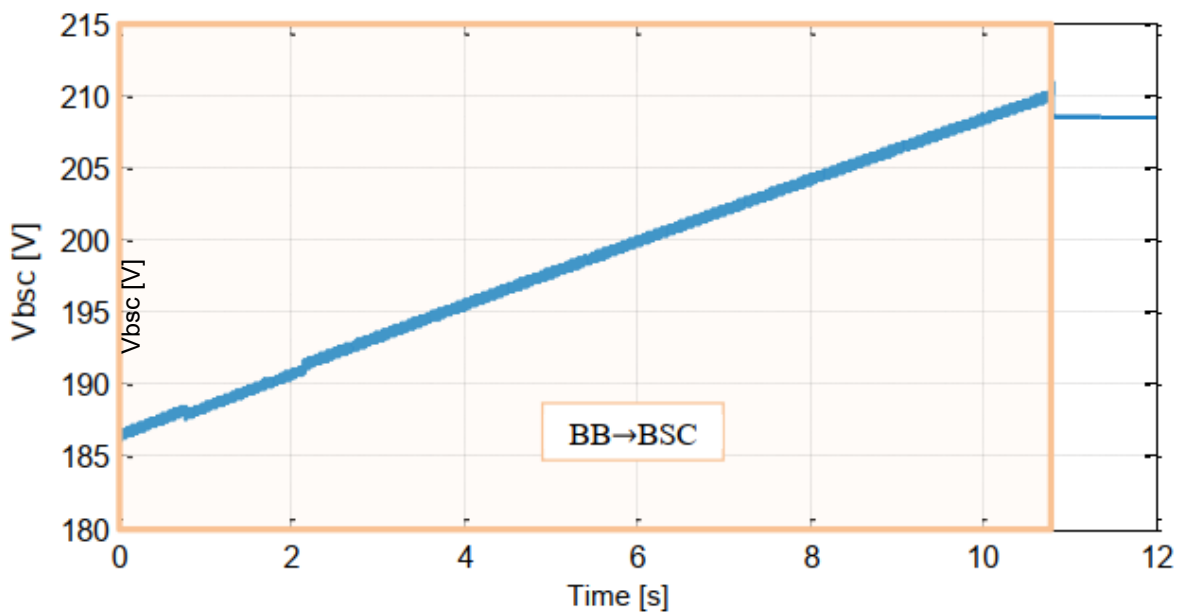


Figure 13 Bank of supercapacitors in the process of charging

Each of these values comes from regenerative braking processes carried out with currents from 1A to 6A, and also considers the non-idealities of semiconductors, which change depending on the current processed. In addition, the Initial conditions are the same as those initially adopted, i.e., $V_{bsc} = 233V$, $T_m = 0.5N.m$.

As can be seen, the values from the simulation are very close to those calculated by the equations, where the average error concerning the yield was 4.11%. An interesting fact arises: the regenerated energy increases as more current is absorbed from the armature, but the maximum occurs at $I_a = 3\text{A}$ for both cases. The performance follows the same behavior in the simulated system, but in the theoretical system, it remains maximum in a region delimited by currents between 3A and 4A. For better visualization, the graph in Figure 14 was assembled.

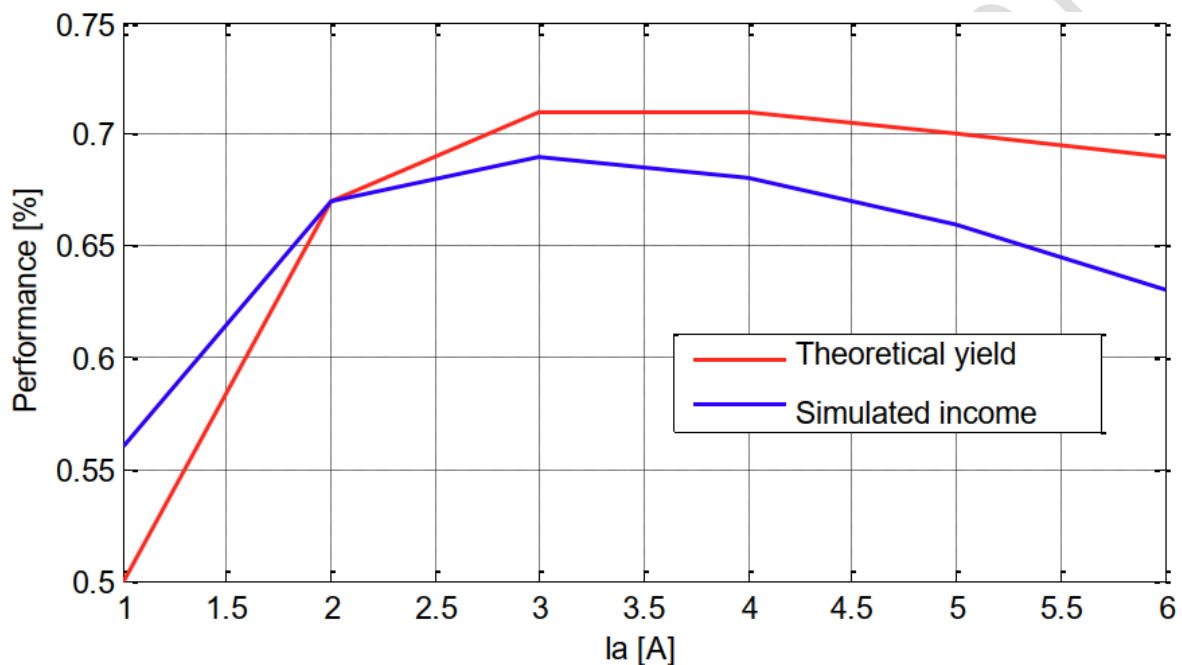


Figure 14 Comparison between theoretical and simulated yields

The regenerated energy and the yield resulting from the simulation are lower than the respective theoretical ones. The probable reason for this difference is related to the simulation itself, where the circuits of the bidirectional power converters and the DC motor, inserted in the PSIM® software, are imported by Simulink®. The signs indicated in Figure 14 show the probable source of this difference.

Figure 15 represents a small interval taken from the period in which regenerative braking occurs, where the voltage and current behaviors in the T4 transistor are found during the conduction, locking, and switching phases.

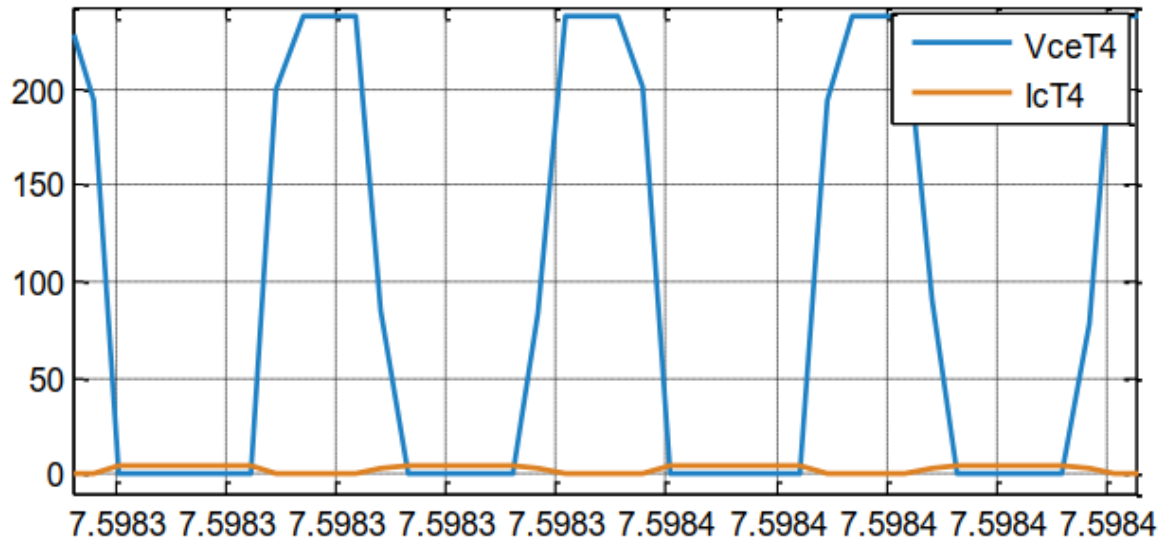


Figure 15 Voltage (V_{ceT4}) and current (I_{cT4}) waveforms on the T4 transistor

In the conduction phase, the collector current is equal to that of the armature, and the voltage between the collector and emitter terminals migrates to saturation, i.e., at this moment the only source of losses is the product between the two quantities. In the blocking phase, the collector current cancels out and the transistor supports the voltage of the supercapacitor bank, a situation where losses can be disregarded. Finally, there is the crossover between V_{bsc} and I_a in the switching phase, as the transistor takes some time to start and stop conduction. Such crossover causes the switching losses already mentioned in the study, and they are probably responsible for the small divergence of the data presented in Table 18, since it is not possible to configure the parameters of the transistors that are part of the converter circuit regarding the switching times. Thus, the software treats them according to some method, and this does not match the behavior considered in the theoretical calculations

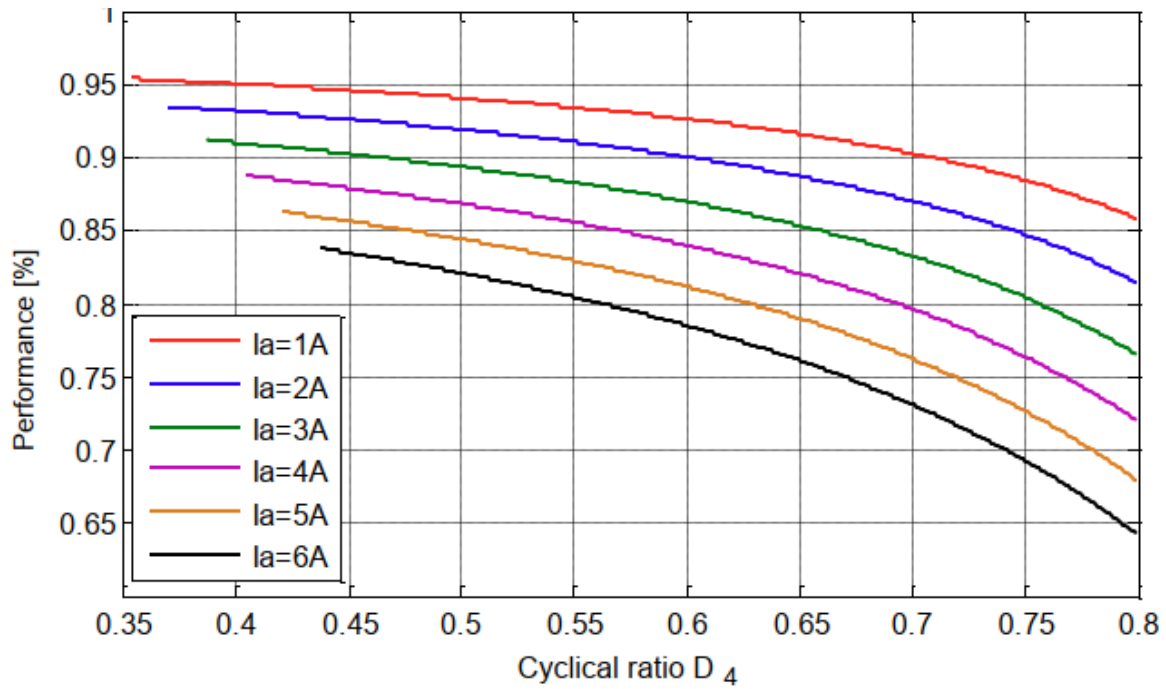


Figure 16 Yield of bi-directional converter 2 during braking for various I_a values.

Still referring to performance, it is also interesting to see how the bidirectional converter 2 behaves during regenerative braking. To discover its peculiarities, it is enough to express this magnitude in a graph as a function of the variation of D_4 for various currents absorbed from the reinforcement, as shown in the graph in Figure 16.

It is observed that the yield decreases as the cycle ratio increases, a behavior that is characteristic of the converter Boost. In addition, it is also reduced as greater braking capacity is required, i.e., higher A_i values, as the power dissipation in the armor's resistance increases with its increase. Another point that deserves to be highlighted in the graph in Figure 16 corresponds to the different values of the initial cyclic ratios for each absorbed current. The explanation for this phenomenon comes from the investigation of the terminal voltage of the DC motor, which represents the power supply to the converter Boost. The terminal voltage is obtained by the difference between the counterelectromotive force E_a and the product $R_a I_a$, i.e., the greater the current drained from the armor, the lower it will be V_t , thus implying the increase of D_4 .

In steady-state cruising conditions, the batteries supplied the continuous current required to overcome motor and load losses. The voltage and current profiles showed good stability in this operating mode. The passive balancing circuits effectively avoided individual cell over-

voltage during the tests.

The motor load was stepped up for the regenerative braking tests to initiate deceleration. The resulting decrease in speed caused the motor to operate as a generator, transferring power back to the supercapacitors. Recovered energy on the capacitors reached over 1200J, with system efficiency up to 65%. This demonstrates the ability to capture a significant portion of the vehicle's kinetic energy during braking.

The prototype exhibited some discrepancies between the simulated and experimental results, primarily lower efficiency during regeneration. Sources of losses include inaccuracies in model parameters, unmodeled dynamics like converter switching losses, and limitations of the digital control platform. The relatively high series resistance of the battery bank also restricted the operating current range.

However, the key functionality of the hybrid storage system and bidirectional converters was successfully validated. The supercapacitors handled current transients while the batteries provided steady power. The dSPACE controller effectively coordinated the switching of the converters in response to operating conditions and commanded current references. The results illustrate the viability of this architecture to overcome issues like limited range and battery stress in electric vehicles.

In conclusion, the prototype implemented and demonstrated the core concepts of hybrid energy storage and bidirectional power flow for electric vehicle applications. The modular design allows flexibility in the battery and supercapacitor banks to meet different voltage and energy capacity requirements. Further refinement of the system parameters and controls may improve efficiency and dynamic response. With improved range and battery longevity, adoption of electric vehicles could be accelerated by utilizing architectures based on this work.

5 Conclusions

This work presented the design, implementation, and experimental evaluation of a hybrid energy storage system for electric vehicles using supercapacitors and batteries. Bidirectional DC-DC converters were developed to actively control power flow between the energy sources and the traction motor. Regenerative braking capabilities were incorporated to recover kinetic energy during deceleration for efficiency improvements.

The use of supercapacitors as a complementary storage element to batteries helps overcome critical limitations of electric vehicles. The supercapacitors can supply high transient currents

during acceleration, avoiding battery overload. They also accept charge efficiently during braking, unlike batteries, which can be damaged by sudden charging. This hybrid architecture allows the batteries to operate in an optimal steady power range.

Simulation models were created in MATLAB/Simulink to facilitate system design and testing. The control logic was deployed on a dSPACE real-time controller for hardware-in-the-loop experimentation. Custom circuit boards implemented the converters, gate drivers, sensors, and auxiliary systems. The laboratory test bench provided a flexible platform for evaluating the prototype.

Experimental results successfully validated the core functionality, including the coordination of converter switching to manage power flow. The supercapacitors supplied momentary acceleration loads over 8A, while the batteries were provided under 2A. Regenerative braking recovered over 1200J, with system efficiency up to 65%, reasonably matching predictions. This demonstrated the viability of the hybrid supply and bidirectional converters.

Opportunities for improvement remain, including mitigating the lower efficiency compared to simulations. Advanced controls may also expand the operational range. With further refinement, this architecture can help overcome barriers to electric vehicle adoption such as limited range and battery life. The prototyping approach presented enables rapid design iteration and flexibility. Overall this work validated the benefits of hybrid energy storage with reversible converters for sustainable and efficient transportation.

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